DESCRIPTION

CEL’s FreeStar Pro module provides a high performance and cost effective RF transceiver solution for 2.4 GHz IEEE 802.15.4, ZigBee, and Zigbee PRO wireless networks.

The FreeStar Pro module is based on the Freescale™ MC13224V (ZFSM-201-1) or MC13226V (ZFSM-201-2) transceiver platforms. It combines Freescale’s transceiver IC with an onboard 100mW Power Amplifier. Ideal for remote sensing, AMR/AMI, home and building automation, industrial control, and security applications, FreeStar Pro combines extensive processing capability with high output power and low power consumption.

The processing power of the MC1322xV enables the FreeStar Pro to provide a level of integration unprecedented in a ZigBee module. The 32-bit ARM7TDMI processor and expansive on-chip memory enable designers to eliminate the peripheral host processors often required by 8- and 16-bit transceiver solutions. This high level of integration reduces component count, lower power consumption and overall system costs.

FEATURES
- Powerful 32-bit ARM7TDMI based microprocessor
- Extensive on-board memory resources
- Up to 100 mW output power
- Miniature footprint: 1” x 1.4” (25.4 mm x 36.5 mm)
- Integrated PCB trace antenna
- Optional MMCX connector for external antenna
- 15 RF channels
- Over 4000 feet of range
- AES 128-bit encryption
- Low power consumption
- FCC, CE and IC certified
- RoHS compliant

APPLICATIONS

Automated Meter Reading
- In meter applications
- Thermostats
- In-home display units

Home & Building Automation
- Security
- HVAC control
- Lighting control
- Thermostats

Industrial Controls
- Food processing controls
- Traffic Management
- Sensor Networks
- Asset Management
- Barcode reader
- Patient Monitoring
- Glucose monitor

ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Order Number</th>
<th>Description</th>
<th>Mins/Mults</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZFSM-201-1</td>
<td>FreeStarPro MC13224 100mW transceiver module PCB Trace Antenna</td>
<td>1,960 pcs / 140 pcs</td>
<td></td>
</tr>
<tr>
<td>ZFSM-201-1C</td>
<td>FreeStarPro MC13224 100mW transceiver module with MMCX connector</td>
<td>1,960 pcs / 140 pcs</td>
<td></td>
</tr>
<tr>
<td>ZFSM-201-2</td>
<td>FreeStarPro MC13226 100mW transceiver module PCB Trace Antenna</td>
<td>1,960 pcs / 140 pcs</td>
<td></td>
</tr>
<tr>
<td>ZFSM-201-2C</td>
<td>FreeStarPro MC13226 100mW transceiver module with MMCX connector</td>
<td>1,960 pcs / 140 pcs</td>
<td></td>
</tr>
<tr>
<td>ZFSM-201-KIT-1</td>
<td>FreeStar Pro Module Kit</td>
<td>1</td>
<td></td>
</tr>
</tbody>
</table>

The information in this document is subject to change without notice, please confirm data is current

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EVALUATION KIT

The FreeStar Pro Kits assists users in both evaluation and development. As a stand alone radio system, the kit allows users to place the modules into the target environment and evaluate performance. The FreeStar Pro kit also serves as an invaluable aid in application development. Through the many headers on the interface board, the user has access to all pins on the ZFSM-201-1 enabling easy connection to the target system for application development.

The FreeStar Pro module contains the Freescale™ MC1322xV transceiver IC, an NEC high gain Power Amplifier, XTALs, Power Regulator, and an integrated PCB antenna.

The interface board features a serial communication interface, a power management module, peripherals such as potentiometer LEDs, and GPIO headers. The Evaluation Kit also contains four AA batteries and two USB cables.

For more detailed information regarding FreeStar Pro Evaluation Kit, refer to the FreeStar Pro Evaluation Kit User Guide document. (Available at CEL's website http://www.cel.com)

Kit Contents:

- Three Evaluation Boards with ZFSM-201-1 Modules
- Two USB A/B Cables
- Ten Jumpers (Spares)
- Four AA Batteries
- Software & Technical Information CD
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MICROPROCESSOR

The primary component of the FreeStar Pro module is Freescale’s third generation ZigBee platform. It incorporates a complete, low power, 2.4 GHz radio frequency transceiver, 32-bit ARM7TDMI-based microprocessor, hardware acceleration for both IEEE 802.15.4 MAC and AES security plus a full suite of processor peripherals.

The MC1322xV architecture offers superior processing power for ZigBee applications. The core operates up to 26 MHz. An 80 kByte ROM is utilized for the low level IEEE 802.15.4 MAC and PHYsical layer commands. This off loads the Flash memory, leaving more space for the end user application. The MC1322xV supports 128 kBytes of Flash memory. The program code is mirrored in 96 kBytes of RAM for faster execution by the processor core. A full set of peripherals and Direct Memory Access (DMA) capability for transceiver packet data are also included.

In addition, the MC1322xV provides extensive power savings options, including low current sleep modes allowing for maximum operating life when battery-powered.

The MC1322x family is available as two part numbers. These device types differ only in their ROM contents, all other device hardware, performance, and specifications are identical:

- **MC13224V** - this is the original version and is the generic part type.
  - The MC13224V is intended for most IEEE 802.15.4 applications including MAC-based, ZigBee-2007 Profile 1, and ZigBee RF4CE targets.
  - It has a more complete set of peripheral drivers in ROM.

- **MC13226V** - this is a more recent version and is provided specifically for ZigBee-2007 Profile 2 (Pro) applications. Only the onboard ROM image has been changed to optimize ROM usage for the ZigBee Pro profile and maximize the amount of available RAM for application use.
  - The IEEE MAC/PHY functionality has been streamlined to include only that functionality required by the ZigBee specification. The MAC functionality is 802.15.4 compatible.
  - For a typical application, up to 20 kbytes more of RAM is available versus the M13224V.
  - Some drivers present in the MC13224 ROM have been removed and these include the ADC, LCD font, and SSI drivers. These drivers are still available as library functions, but now compile into the RAM space.
  - The Low Level Component (LLC) functionality has also been streamlined for the ZigBee Specification.

ANTENNA

FreeStar Pro modules include an integrated PCB trace antenna. An optional MMCX connector can be specified, enabling connection to a 50-ohm external antenna of the user’s choice. See Ordering Information on page 1.

The PCB antenna employs an F-Antenna topology that is compact and supports an omni-directional radiation pattern. To maximize antenna efficiency, an adequate ground plane must be provided on the host PCB. Correctly positioned, the ground plane on the host board under the module will contribute significantly to antenna performance.

The position of the module on the host board and overall design of the product enclosure contribute to antenna performance. Poor design affects radiation patterns and can result in reflection, diffraction, and/or scattering of the transmitted signal.

Here are some design guidelines to help ensure antenna performance:

- Never place the ground plane or route copper traces directly underneath the antenna portion of the module.
- Never place the antenna close to metallic objects.
- In the overall design, ensure that wiring and other components are not placed near the antenna.
- Do not place the antenna in a metallic or metallized plastic enclosure.
- Keep plastic enclosures 1cm or more from the antenna in any direction.
MODES OF OPERATION

FreeStar Pro power management is controlled through the Freescale MC1322xV’s Clock and Reset Module (CRM). The CRM is a dedicated module to handle clock, reset, and power management functions including control of the power regulators. All these functions have direct impact on attaining the lowest power.

The FreeStar Pro module supports three modes of operation: Active, Doze and Hibernation. The latter two modes are the low-power sleep modes.

Active Mode
In this mode all functions / features are operating normally.

Doze Mode
Doze mode provides significant reduction in power consumption while still maintaining a high degree of sleep timing accuracy. In Doze mode, the reference oscillator of the processor continues to operate normally.

Hibernation Mode
Hibernation mode provides the greatest reduction in power consumption however the sleep timing accuracy is not as precise as in Doze mode.

The CRM manages the recovery from the low-power modes, similar to power-up from reset, providing regulator and clock management.

The module can be awoken from the low-power modes in 3 ways, wake-up can occur:
  • On external interrupts through any of the 4 Keyboard Interface inputs
  • From internal interrupts
  • On the Real Time (wake-up) timer interrupt

For more detail information on modes of operation refer to Freescale’s MC1322xV datasheet available at Freescale’s website (www.freescale.com)
POWER AMPLIFIER

The FreeStar Pro module incorporates a high performance Power Amplifier from Renesas Electronics.

Power Amplifier Control Line

FreeStar Pro modules include a separate 1.8V regulator supplying a bias that enables consistent module output performance over the wide operating range. To prevent excessive sleep current draw, this regulator should be disabled when the module is placed into sleep mode. The voltage regulator is controlled by GPIO42 (ANT1), setting GPIO42 high enables the regulator while setting GPIO42 low disables the regulator. See the table below for the Turn on Time requirements for the voltage regulator.

SPECIFICATIONS — GPIO42

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Regulator enable voltage</td>
<td>0.95</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Regulator disable voltage</td>
<td>0.40</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Turn on Time for VOUT=1.8V (Default)</td>
<td>105</td>
<td>200</td>
<td>µsec</td>
<td></td>
</tr>
</tbody>
</table>

For the TX_ON and RX_ON pins, the function table is as follows:

<table>
<thead>
<tr>
<th>Parameter</th>
<th>TX Mode</th>
<th>RX Mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>TX_ON</td>
<td>High</td>
<td>Low</td>
</tr>
<tr>
<td>RX_ON</td>
<td>Low</td>
<td>High</td>
</tr>
</tbody>
</table>

Other Notes:
- The GPIO43 (ANT2) pin is not used and is left unconnected.
- Due to the long turn on time (105µs) of the regulator, the regulator is enabled or ON all the time with the exception of sleep mode.
- The RF switch uses both the TX_ON and RX_ON outputs as control inputs.
- The PA uses the TX_ON line as the control input.

INTERFACE

The FreeStar Pro module has all major pins routed to the castellation connectors, this includes, but is not limited, to the pins for JTAG, serial communication, A/D, etc.

HOST PROTOCOL INTERFACE COMMANDS

CEL provides the Host Serial and RF Protocols document which details the protocols and commands between the Host processor (i.e. an external microprocessor, a PC, etc.) and the FreeStar Pro module. An example of the commands, but not limited to, included in the host protocol interface are as follows:

- Query Version (MAC version)
- Set RF Channel
- Set RF Power
- Transmit Packet Error Test

For more detail refer to Host Serial and RF Protocols document listed on our website at http://www.cel.com (FreeStar Pro Host & RF Protocol)
### ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Description</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Voltage</td>
<td>-0.3</td>
<td>3.6</td>
<td>VDC</td>
</tr>
<tr>
<td>Voltage on Any Digital Pin</td>
<td>-0.3</td>
<td>VCC + 0.2</td>
<td>VDC</td>
</tr>
<tr>
<td>RF Input Power</td>
<td>10</td>
<td></td>
<td>dBm</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>-45</td>
<td>125</td>
<td>°C</td>
</tr>
<tr>
<td>Reflow Soldering Temperature</td>
<td></td>
<td>260</td>
<td>°C</td>
</tr>
</tbody>
</table>

**Note:** Exceeding the maximum ratings may cause permanent damage to the module.

### RECOMMENDED OPERATING CONDITIONS

<table>
<thead>
<tr>
<th>Description</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Voltage (VCC)</td>
<td>2.1</td>
<td>3.6</td>
<td>VDC</td>
<td></td>
</tr>
<tr>
<td>Ambient Temperature Range</td>
<td>-40</td>
<td>25</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>Crystal Reference Oscillator</td>
<td></td>
<td>24</td>
<td>MHz</td>
<td></td>
</tr>
</tbody>
</table>

### DC CHARACTERISTICS (@ 25°C, VCC = 3.3V unless otherwise noted)

<table>
<thead>
<tr>
<th>Description</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Transmit Mode Current (at +20 dBm Output Power)</td>
<td>193</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Receive Mode Current</td>
<td>30</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Hibernate or Doze Mode Current</td>
<td>5</td>
<td></td>
<td>µA</td>
</tr>
</tbody>
</table>

### RF CHARACTERISTICS (@ 25°C, VCC = 3.3V unless otherwise noted)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>General Chacteristics</strong></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>RF Frequency Range</td>
<td>2400</td>
<td>2483.5</td>
<td>MHz</td>
<td></td>
</tr>
<tr>
<td>RF Data Rate</td>
<td>250</td>
<td></td>
<td>kbps</td>
<td></td>
</tr>
<tr>
<td><strong>Transmitter</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Nominal Output Power</td>
<td>20</td>
<td></td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Programmable Output Power Range</td>
<td>18</td>
<td></td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Error Vector Magnitude</td>
<td>8</td>
<td>35</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td><strong>Receiver</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Receiver Sensitivity (1% PER)</td>
<td>-92</td>
<td>-94</td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>Saturation (Maximum Input Level) (1% PER)</td>
<td>0</td>
<td></td>
<td>dBm</td>
<td></td>
</tr>
<tr>
<td>802.15.4 Adjacent Channel Rejection (±5 MHz)</td>
<td>35</td>
<td>40</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>802.15.4 Alternate Channel Rejection (±10 MHz)</td>
<td>50</td>
<td></td>
<td>dB</td>
<td></td>
</tr>
</tbody>
</table>

### PIN SIGNALS I/O PORT CONFIGURATION

The FreeStar Pro module has 60 edge I/O interfaces for connection to the user's host board. Figure 1 shows the layout of the 60 edge castellations.
<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
<th>IC Pin #</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>GND</td>
<td>GND</td>
<td>GND</td>
<td>85</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
<td>GND</td>
<td>GND</td>
<td>84</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>GND</td>
<td>GND</td>
<td>75</td>
</tr>
</tbody>
</table>
| 4     | ADC2_VREFL       | Analog Input or Digital I/O | GPIO39
Alternate function: Low reference voltage for ADC2 | 61       |
| 5     | ADC1_VREFL       | Analog Input or Digital I/O | GPIO41
Alternate function: Low reference voltage for ADC1 | 62       |
| 6     | ADC1_VREFH       | Analog Input or Digital I/O | GPIO40
Alternate function: High reference voltage for ADC1 | 63       |
| 7     | ADC2_VREFH       | Analog Input or Digital I/O | GPIO38
Alternate function: Low reference voltage for ADC2 | 64       |
| 8     | ADC0             | Analog Input or Digital I/O | GPIO30
Alternate function: ADC analog input Channel 0 | 1        |
| 9     | ADC1             | Analog Input or Digital I/O | GPIO31
Alternate function: ADC analog input Channel 1 | 2        |
| 10    | ADC2             | Analog Input or Digital I/O | GPIO32
Alternate function: ADC analog input Channel 2 | 3        |
| 11    | ADC3             | Analog Input or Digital I/O | GPIO33
Alternate function: ADC analog input Channel 3 | 4        |
| 12    | VCC              | Power Input           | High side supply voltage to buck regulator switching MOSFET & IO buffers | 45       |
| 13    | ADC4             | Analog Input or Digital I/O | GPIO34
Alternate function: ADC analog input Channel 4 | 5        |
| 14    | ADC5             | Analog Input or Digital I/O | GPIO35
Alternate function: ADC analog input Channel 5 | 6        |
| 15    | ADC6             | Analog Input or Digital I/O | GPIO36
Alternate function: ADC analog input Channel 6 | 7        |
| 16    | ADC7_RTCK        | Analog Input or Digital I/O | GPIO37
Alternate function: ADC analog input Channel 7 / Return Clock | 8        |
| 17    | TDO              | Digital I/O           | GPIO49
Alternate function: JTAG Test Data Output | 9        |
| 18    | TDI              | Digital I/O           | GPIO48
Alternate function: JTAG Test Data Input | 10       |
| 19    | TCK              | Digital I/O           | GPIO47
Alternate function: JTAG Test Clock Input | 11       |
| 20    | TMS              | Digital I/O           | GPIO46
Alternate function: JTAG Test Mode Select Input | 12       |
| 21    | UART2_RTS        | Digital I/O           | GPIO21
Alternate function: UART2 Request to Send input | 13       |
| 22    | GND              | GND                   | GND                                                      | 76       |
| 23    | UART2_CTS        | Digital I/O           | GPIO20
Alternate function: UART2 Clear to Send output | 14       |
| 24    | UART2_RX         | Digital I/O           | GPIO19
Alternate function: UART2 RX data input | 15       |
| 25    | UART2_TX         | Digital I/O           | GPIO18
Alternate function: GPIO18UART2 TX data output | 16       |
| 26    | UART1_RTS        | Digital I/O           | GPIO17
Alternate function: UART1 Request to Send input | 17       |
| 27    | UART1_CTS        | Digital I/O           | GPIO16
Alternate function: UART1 Clear to Send output | 18       |
| 28    | I2C_SDA          | Digital I/O           | GPIO13
Alternate function: I2C Bus data | 21       |
### FREESTAR PRO I/O PIN ASSIGNMENTS (Continued)

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
<th>IC Pin #</th>
</tr>
</thead>
<tbody>
<tr>
<td>29</td>
<td>I2C_SCL</td>
<td>Digital I/O</td>
<td>GPIO12 Alternate function: I2C Bus clock</td>
<td>22</td>
</tr>
<tr>
<td>30</td>
<td>TMR3</td>
<td>Digital I/O</td>
<td>GPIO11 Alternate function: Timer 3 IO signal</td>
<td>23</td>
</tr>
<tr>
<td>31</td>
<td>VCC</td>
<td>Power Input</td>
<td>High side supply voltage to buck regulator switching MOSFET &amp; IO buffers</td>
<td>45</td>
</tr>
<tr>
<td>32</td>
<td>TMR2</td>
<td>Digital I/O</td>
<td>GPIO10 Alternate function: Timer 2 IO signal</td>
<td>24</td>
</tr>
<tr>
<td>33</td>
<td>TMR1</td>
<td>Digital I/O</td>
<td>GPIO9  Alternate function: Timer 1 IO signal</td>
<td>25</td>
</tr>
<tr>
<td>34</td>
<td>TMR0</td>
<td>Digital I/O</td>
<td>GPIO8  Alternate function: Timer 0 IO signal</td>
<td>26</td>
</tr>
<tr>
<td>35</td>
<td>SPI_SCK</td>
<td>Digital I/O</td>
<td>GPIO7  Alternate function: SPI Port clock</td>
<td>27</td>
</tr>
<tr>
<td>36</td>
<td>UART1_TX</td>
<td>Digital I/O</td>
<td>GPIO14 Alternate function: UART1 TX data output</td>
<td>20</td>
</tr>
<tr>
<td>37</td>
<td>UART1_RX</td>
<td>Digital I/O</td>
<td>GPIO15 Alternate function: UART1 RX data input</td>
<td>19</td>
</tr>
<tr>
<td>38</td>
<td>GND</td>
<td>GND</td>
<td>GND</td>
<td>77</td>
</tr>
<tr>
<td>39</td>
<td>SPI_MOSI</td>
<td>Digital I/O</td>
<td>GPIO6  Alternate function: SPI Port MOSI</td>
<td>28</td>
</tr>
<tr>
<td>40</td>
<td>SPI_MISO</td>
<td>Digital I/O</td>
<td>GPIO5  Alternate function: SPI Port MISO</td>
<td>29</td>
</tr>
<tr>
<td>41</td>
<td>SPI_SS</td>
<td>Digital I/O</td>
<td>GPIO4  Alternate function: SPI Port SS</td>
<td>30</td>
</tr>
<tr>
<td>42</td>
<td>SSI_BITCK</td>
<td>Digital I/O</td>
<td>GPIO3  Alternate function: SSI Bit Clock</td>
<td>31</td>
</tr>
<tr>
<td>43</td>
<td>SSI_FSYN</td>
<td>Digital I/O</td>
<td>GPIO2  Alternate function: SSI Frame Sync</td>
<td>32</td>
</tr>
<tr>
<td>44</td>
<td>SSI_RX</td>
<td>Digital I/O</td>
<td>GPIO1  Alternate function: SSI RX data input</td>
<td>33</td>
</tr>
<tr>
<td>45</td>
<td>SSI_TX</td>
<td>Digital I/O</td>
<td>SSI TX data output / GPIO0</td>
<td>34</td>
</tr>
<tr>
<td>46</td>
<td>KBI_7</td>
<td>Digital I/O</td>
<td>GPIO29 Alternate function: Keyboard Interface Bit 7</td>
<td>35</td>
</tr>
<tr>
<td>47</td>
<td>COIL_BK</td>
<td>Power Switch</td>
<td>Buck Converter coil drive output</td>
<td>43</td>
</tr>
<tr>
<td>48</td>
<td>KBI_6</td>
<td>Digital I/O</td>
<td>GPIO28 Alternate function: Keyboard Interface Bit 6</td>
<td>36</td>
</tr>
<tr>
<td>49</td>
<td>RESETB</td>
<td>Digital Input</td>
<td>System reset input</td>
<td>51</td>
</tr>
<tr>
<td>50</td>
<td>LREG_BK_FB</td>
<td>Power Input</td>
<td>Voltage input to onboard regulators, buck regulator feedback voltage</td>
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<td>KBI_5</td>
<td>Digital I/O</td>
<td>GPIO27 Alternate function: Keyboard Interface Bit 5</td>
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<td>KBI_4</td>
<td>Digital I/O</td>
<td>GPIO26 Alternate function: Keyboard Interface Bit 4</td>
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<td>KBI_3</td>
<td>Digital I/O</td>
<td>GPIO25 Alternate function: Keyboard Interface Bit 3</td>
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<td>KBI_2</td>
<td>Digital I/O</td>
<td>GPIO24 Alternate function: Keyboard Interface Bit 2</td>
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<td>KBI_1</td>
<td>Digital I/O</td>
<td>GPIO23 Alternate function: Keyboard Interface Bit 1</td>
<td>41</td>
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<td>57</td>
<td>KBI_0_HST_WK</td>
<td>Digital I/O</td>
<td>GPIO22 Alternate function: Keyboard Interface Bit 0 / Host Walk-up output</td>
<td>42</td>
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<tr>
<td>58</td>
<td>GND</td>
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<td>GND</td>
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<td>GND</td>
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<td>87</td>
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</tbody>
</table>
MODULE DIMENSIONS  Dimensions in inches. Tolerances = +/-0.005" unless otherwise noted.

FreeStar PRO ZFSM-201-x

For layout recommendation for optimum antenna performance, refer to Antenna section in this document.
MODULE LAND FOOTPRINT

NOTE: Dimensions in inches [mm]. Tolerances = +/-0.005" unless otherwise noted.

Figure 2

NOT RECOMMENDED FOR NEW DESIGN
PROCESSING

Recommended Reflow Profile

Parameters Values
Ramp up rate (from Tsoakmax to Tpeak) 3º/sec max
Minimum Soak Temperature 150ºC
Maximum Soak Temperature 200ºC
Soak Time 60-120 sec
TLiquidus 217ºC
Time above TL 60-150 sec
Tpeak 250ºC
Time within 5º of Tpeak 20-30 sec
Time from 25º to Tpeak 8 min max
Ramp down rate 6ºC/sec max

Achieve the brightest possible solder fillets with a good shape and low contact angle.

Pb-Free Soldering Paste

Use of “No Clean” soldering paste is strongly recommended, as it does not require cleaning after the soldering process.

Note: The quality of solder joints on the castellations (‘half vias’) where they contact the host board should meet the appropriate IPC Specification. See IPC-A-610 Acceptability of Electronic Assemblies, section 8.2.4 “Castellated Terminations.”

Cleaning

In general, cleaning the populated modules is strongly discouraged. Residuals under the module cannot be easily removed with any cleaning process.

- Cleaning with water can lead to capillary effects where water is absorbed into the gap between the host board and the module. The combination of soldering flux residuals and encapsulated water could lead to short circuits between neighboring pads. Water could also damage any stickers or labels.
- Cleaning with alcohol or a similar organic solvent will likely flood soldering flux residuals into the two housings, which is not accessible for post-washing inspection. The solvent could also damage any stickers or labels.
- Ultrasonic cleaning could damage the module permanently.

The best approach is to consider using a “no clean” soldering paste and eliminate the post-soldering cleaning step.

Optical Inspection

After soldering the Module to the host board, consider optical inspection to check the following:

- Proper alignment and centering of the module over the pads.
- Proper solder joints on all pads.
- Excessive solder or contacts to neighboring pads, or vias.

Repeating Reflow Soldering

Only a single reflow soldering process is encouraged for host boards.

Wave Soldering

If a wave soldering process is required on the host boards due to the presence of leaded components, only a single wave soldering process is encouraged.

Hand Soldering

Hand soldering is possible. Use a soldering iron temperature setting equivalent to 350ºC, follow IPC recommendations/reference document IPC-7711.
AGENCY CERTIFICATIONS

FCC Part 15.247 Module Certified (Mobile)
The Freestar Pro modules comply with Part 15 of the Federal Communications Commission rules and regulations and with Industry Canada license exempt RSS standards.
To meet the Certification requirements, the user must meet these regulations:

- The text on the FCC ID label provided with the module must be placed on the outside of the final product.
- The modules may only use the antennas that have been tested and approved with these modules:
  - The on-board PCB trace antenna
  - Nearson S181AH-2450S antenna.

Per Section 2.109, the Freestar Pro module has been certified by the FCC for use with other products without additional certification. Any modifications to this product may violate the rules of the Federal Communications Commission and make operation of the product unlawful.

Per Sections 15.107 and 15.109, the user’s end product must be tested for unintentional radiators compliance.

RF Output Power Setting
In order to comply with FCC and Industry Canada emissions requirements, the Freestar Pro module has the following power shaping requirements implemented:

- Order Numbers ZFSM-201-1, ZFSM-201-2 which use the integrated PCB antenna, are limited to power step 4 for channels 11 & 12 with channel 26 being disabled.

- Order Numbers ZFSM-201-1C, ZFSM-201-2C which has the MMCX connector, has firmware that limits the power to CEL Test Tool setting 4 on channels 11-25, with channel 26 being disabled.

In addition to restricting power, the firmware also limits the maximum data payload to 106 bytes for any transmitted packet. The firmware also limits the transmission rate such that a maximum duty cycle of 32 percent is allowed.

RF Output Power

<table>
<thead>
<tr>
<th>Certification</th>
<th>RF Channel</th>
<th>CEL Test Tool</th>
<th>Typical Max Output Power</th>
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</thead>
<tbody>
<tr>
<td>ZFSM-201-1</td>
<td>FCC/IC</td>
<td>11,12</td>
<td>4</td>
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<tr>
<td>ZFSM-201-2</td>
<td>FCC/IC</td>
<td>13-25</td>
<td>6</td>
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<tr>
<td>ZFSM-201-1C</td>
<td>ETSI</td>
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<tr>
<td>ZFSM-201-2C</td>
<td>ETSI</td>
<td>11-25</td>
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</table>
AGENCY CERTIFICATIONS (Continued)

FCC Compliance Statement (Part 15.19) Section 7.15 of RSS-GEN
This device complies with Part 15 of the FCC Rules and with Industry Canada license exempt RSS Standards. Operation is subject to the following two conditions:
1. This device may not cause harmful interference.
2. This device must accept any interference received, including interference that may cause undesired operation.

Warning (Part 15.21)
Changes or modifications not expressly approved by CEL could void the user’s authority to operate the equipment.

20 cm Separation Distance
To comply with FCC/IC RF exposure limits for general population / uncontrolled exposure, the antenna(s) used for this transmitter must be installed to provide a separation distance of at least 20 cm from all persons and must not be co-located or operating in conjunction with any other antenna or transmitter.

OEM Responsibility to the FCC Rules and Regulations
The FreeStar Pro Module has been certified per FCC Part 15 rules and to Industry Canada license-exempt RSS Standards for integration into products without further testing or certification. To fulfill the FCC and IC certification requirements, the OEM using the FreeStar Pro Module must ensure that the information provided on the FreeStar Pro Label is placed on the outside of the final product. The FreeStar Pro Module is labeled with its own FCC ID Number and IC ID Number. If the FCC ID is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use wording such as the following: “Contains Transmitter Module FCC ID: W7Z-FSTARPRO” or “Contains FCC ID: W7Z-FSTARPRO”

The OEM using the FreeStar Pro Module must only use the approved antennas, (PCB Trace Antenna or Nearson Whip Antenna Model S181AH-2450S) that has been certified with this module. The OEM using the FreeStar Pro Module must test their final product configuration to comply with Unintentional Radiator Limits before declaring FCC compliance per Part 15 of the FCC rules.

IC Certification — Industry Canada Statement
The term “IC” before the certification / registration number only signifies that the Industry Canada technical specifications were met.

Certification IC - Déclaration d’Industrie Canada
Le terme “IC” devant le numéro de certification / d’enregistrement signifie seulement que les spécifications techniques Industrie Canada ont été respectées.

Section 14 of RSS-210
The installer of this radio equipment must ensure that the antenna is located or pointed such that it does not emit RF field in excess of Health Canada limits for the general population. Consult Safety Code 6, obtainable from Health Canada's website: http://www.hc-sc.gc.ca/ewh-semt/pubs/radiation/99ehd-dhm237/index-eng.php

L’article 14 du CNR-210
Le programme d’installation de cet équipement radio doit s’assurer que l’antenne est située ou orientée de telle sorte qu’il ne pas émettre de champ RF au-delà des limites de Santé Canada pour la population générale. Consulter le Code de sécurité 6, disponible sur le site Web de Santé Canada: http://www.hc-sc.gc.ca/ewh-semt/pubs/radiation/99ehd-dhm237/index-eng.php
AGENCY CERTIFICATIONS (Continued)

CE Certification — Europe
The FreeStar Pro RF module has been tested and certified for use in the European Union.

OEM Responsibility to the European Union Compliance Rules
If the FreeStar Pro module is to be incorporated into a product, the OEM must verify compliance of the final product to the European Harmonized EMC and Low-Voltage / Safety Standards. A Declaration of Conformity must be issued for each of these standards and kept on file as described in Annex II of the R&TTE Directive.

The manufacturer must maintain the user's guide and adhere to the settings described in the manual for maintaining European Union Compliance. If any of the specifications are exceeded in the final product, the OEM is required to make a submission to the notified body for compliance testing.

OEM Labeling Requirements
The 'CE' mark must be placed on the OEM product in a visible location.
The CE mark shall consist of the initials “CE” with the following form:

- If the CE marking is reduced or enlarged, the proportions given in the above graduated drawing must be adhered to.
- The CE mark must be a minimum of 5mm in height.
- The CE marking must be affixed visibly, legibly, and indelibly. Since the 2400 - 2483.5 MHz band is not harmonized by a few countries throughout Europe, the Restriction sign must be placed to the right of the 'CE' marking as shown in the picture.

SHIPMENT, HANDLING, AND STORAGE

Shipment
The FreeStar Pro Modules are delivered in trays of 140 pieces.

Handling
The FreeStar Pro Modules are designed and packaged to be processed in an automated assembly line.

Warning
The FreeStar Pro Modules contain highly sensitive electronic circuitry. Handling without proper ESD protection may destroy or damage the module permanently.

Warning
According to JEDEC ISP, the FreeStar Pro Modules are moisture sensitive devices. Appropriate handling instructions and precautions are summarized in Section 2.1. Read carefully to prevent permanent damage due to moisture intake.

Moisture Sensitivity Level (MSL)
MSL 3, per J-STD-033

Storage
Storage/shelf life in sealed bags is 12 months at <40°C and <90% relative humidity.
REFERENCES & REVISION HISTORY

References

<table>
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<th>Reference Documents</th>
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<tr>
<td>Freestar Pro Module Evaluation Kit User Guide</td>
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<tr>
<td>Freescale MC1322xV Datasheet</td>
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<tr>
<td>Freescale Semiconductor MC1322x Reference Manual MC1322xRM</td>
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<tr>
<td>Freescale Semiconductor BeeKit™ User Guide BKWCTKUG</td>
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<tr>
<td>Freescale Semiconductor Software Driver Reference Manual 22XDRVRRM</td>
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<tr>
<td>Freescale Semiconductor Simple Media Access Controller (SMAC) Reference Manual 22xSMACRM</td>
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<td>Freescale Semiconductor Simple Media Access Controller (SMAC) User’s Guide SMACRM</td>
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<td>IAR J-Link and IAR J-Trace user Guide J-Link_J-TraceARM-1</td>
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<td>ARM® IAR Embedded Workbench® IDE User Guide UARM-13</td>
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Revision History

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<td>0006-00-07-00-000</td>
<td>Initial advance datasheet.</td>
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<td>(Preliminary) October 28, 2008</td>
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<td>0006-00-07-00-000</td>
<td>Updated to current revisions to the Freescale Silicon IC</td>
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<td>0006-00-07-00-000</td>
<td>Updated FCC and IC Agency Statements</td>
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<td>(Preliminary) June 11, 2009</td>
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<tr>
<td>0006-00-07-00-000</td>
<td>Updated RF Output Power Setting</td>
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<td>August 17, 2009</td>
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<td>Added the MC13226 (ZFMS-201-2) Transceiver platform Option</td>
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<td>0006-00-07-00-000</td>
<td>Updated FCC and Agency Statements (added French translation), updated RF Output Power information, reformatted and repaginated document</td>
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<td>(Issue B) June 25, 2012</td>
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<tr>
<td>0006-00-07-00-000</td>
<td>Updated Module Land Footprint diagram</td>
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<td>0006-00-07-00-000</td>
<td>Updated Minimum Order Quantity</td>
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<td>(Issue B) November 2, 2012</td>
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